



## DAFTAR PUSTAKA

- [1]. A. Surya, D. Li, T. Isshiki, and H. Kunieda, “PAPER A Fingerprint Matching Using Minutia Ridge Shape for Low Cost Match-on-Card Systems,” *IEICE Trans. Fundam. Electron. Commun. Comput. Sci.*, no. 5, pp. 1305–1312, 2005
- [2]. Mary Lourde R, and Dushyant Khosla, Fingerprint Identification in Biometric Security Systems, *International Journal of Computer and Electrical Engineering*, Vol. 2, No. 5, October, 2010.
- [3]. Dwiyanto, Optimasi Algoritme Ekstraksi Ciri Sidik Jari Berbasis Minutiae Dengan Metode Rutovitz Crossing Number Dan Jumping Search, Universitas Gadjah Mada, 2017.
- [4]. A. Bejo, D. Li, T. Isshiki, and H. Kunieda, “ASIP Design and FPGA Implementation of Fingerprint Authentication Application,” *Int. Conf. Syst. Chip Des. Challenges*, pp. 6–9, 2010.
- [5]. Rahul Kr Das, Abhishek De, Chandrajit Palt, Amlan Chakrabarti, DSP Hardware Design for Fingerprint Binarization and Thinning on FPGA, *International Conferenceon Control, Instrumentation, Energy & Communication (CIEC)* 2014.
- [6]. Dileep Kumar, Yeonseung Ryu, A Brief Introduction of Biometrics and Fingerprint Payment Technology, *International Journal of Advanced Science and Technology* Vol. 4, March, 2009.
- [7]. K. Babu Ravi Teja, Abhilash S. Warrier, Akshay S. Belvadi, Dhiraj R. Gawhane, Design and Implementation of Neighborhood Processing Operations on FPGA using Verilog HDL, *IOSR Journal of VLSI and Signal Processing (IOSR-JVSP)* Volume 4, Issue 1, Ver. II PP 75-80 e-ISSN: 2319 – 4200, p-ISSN No. : 2319 – 4197, 2014
- [8]. O. Kalai Priya, S. Ramasamy, D. Ebenezer, Vlsi Implementation Of Nonlinear Variable Cutoff High Pass Filter Algorithm, *International Conference on Electronics Computer Technology* Volume: 3, 2011 3rd.
- [9]. Tejaswini N C, Karthik P, Design and Implementation of Adaptive Gaussian Filters for the Removal of Salt and Pepper Noise on FPGA, *International Conference on Electrical, Electronics, Communication, Computer and Optimization Techniques (ICEECCOT)* 2016
- [10]. Ramandeep Kaur, Parvinder S. Sandhu , A Novel Method For Fingerprint Feature Extraction, *International Conference on Networking and Information Technology*, 2010
- [11]. Schalkoff, R. J., Digital Image Processing and Computer Vision, *John Wiley & Sons, Inc.* 1989.



- [12]. R. Gonzalez and R. Woods Digital Image Processing, 3rd edition, Prentice Hall, 2008
- [13]. Altera Corporation May 2016, Nios II Processor Reference Handbook, [www.altera.com](http://www.altera.com).
- [14]. Bhuyan, M, and Bhattacharyya, D., An Effective Fingerprint Classification and Search Method. *International Journal of Computer Science and Network Security*, Vol 9, 2009.
- [15]. Jain, K, Hong, L, Pankanti, S, and Bolle, R., An Identity-Authentication System Using Fingerprints. *Proc. IEEE*, vol. 85. 1997
- [16]. Xilinx ISE Design Suite 11 Software Manual, Xilinx, 2009.
- [17]. Verilog Tutorial (online): <http://www.asic-world.com/verilog/veritut.html> 2016.
- [18]. Biometrics Biometric Data Interchange Formats Part 2 : Finger Minutiae Data, no. 40. 2004.
- [19]. C. Wu, "Advanced Feature Extraction Algorithms For Automatic Fingerprint Recognition, Copyright, 2007.
- [20]. Andi Apriadi, Perancangan Otentikasi Sidik Jari Pada Biometric Payment Design Of Authentication Fingerprint For Biometric Payment, *e-Proceeding of Engineering* : Vol.3, No.1 April 2016.
- [21]. Terasic DE0-Nano User Manual World Leading FPGA Based Product and Design Service, [www.terasic.com](http://www.terasic.com) 2016