

DAFTAR PUSTAKA

- Ariyus, D., 2006, “Kriptografi Keamanan Data dan Komunikasi”, Yogyakarta : Graha Ilmu.
- Babu, T.R., Murthy, K.V.V.S., dan Sunil, G., 2011, “AES Algorithm Implementation using ARM Processor”, *IJCA Proceeding on Internationals Conference and workshop on Emerging Trends in Technology (ICWET)(12)* : 24-29, 2011.
- FIPS-197, 2001, “Announcing the Advanced Encryption Standard (AES)”, *Federal Information Processing Standards Publication 197*, <http://csrc.nist.gov/publications/fips/fips-197.pdf>, Diakses 3 April 2021.
- Ghewari, P.B., Pathil, J.K., dan Chougule, A.B., 2010, “Efficient Hardware Design and Implementation of AES Cryptosystem”, *International Journal of Engineering Science and Technology*. Vol. 2(3).
- Gaj, K., Chodowiec, P., 2009, “FPGA and ASIC Implementation of AES”, USA : University Drive.
- Harris, D.M., Harris, S.L., 2013. *Digital Design and Computer Architecture*. Elsevier.
- Kouser, Z., Singhal, M., Joshi, A. A., 2016., “FPGA Implementation of Advanced Encryption Standard Algorithm”, India : Malaviya National Institute of Technology.
- Kurniasari, E., Putra, A.E., Augoestien, N.G., 2019. Implementation of the Montgomery Modular based RSA Algorithm on FPGA. 2019 5th Int. Conf. Sci. Technol.
- Munir, Rinaldhi, 2019, “Kriptografi Edisi ke 2”, Bandung : Informatika Bandung.
- Paar, C., dan Pelzl, Jan. 2010, “Understanding Cryptography”, German : Springer.
- Paul, R., Saha, S., Sau, S., dan Chakrabharti, A., 2012., “Design and Implementation of Real Time AES-128 on Real Time Operating System for Multiple FPGA Communication”.
- Patil, V.B., Bombale, U.L., dan Dixit, P.H., 2013, “Implementation of AES Algorithm on ARM Processor for Wireless Network”. *International Journal of Advanced Research in Computer and Communication Engineering* Vol.2, Issue 8.

Stalling, W., (2014). *Cryptography and Network Security; Principles and Practice Sixth Edition*. USA : Pearson Education, Inc.

Trang, H., dan Loi, N.V., 2012, “An Efficient FPGA Implementation of the Advanced Encryption Standard Algorithm”, *IEEE RIVF International Conference on Computing and Communication Technology* pp 1-4.

Wibowo, Ferry Wahyu, 2014, “FPGA dan VHDL Teori, Antarmuka dan Aplikasi”, Yogyakarta : deepublish.

Xilinx, 2016, “7 Series FPGAs Configurable Logic Block”, https://www.xilinx.com/support/documentation/user_guides/ug474_7Series_CLB.pdf