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Pengembangan Model Real-Time Buck Converter pada FPGA untuk Pengujian Sistem Kendali Berbasis Hardware-in-the-Loop (HIL)
Nathan Bennett Chang, Dr.-Ing. Ir. Yohan Fajar Sidik, S.T., M.Eng.; Dzuhri Radityo Utomo, S.T., M.S., Ph.D.
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