



DAFTAR PUSTAKA

- [1] H. A.-H. Hikmat Abdullah, "Design and Implementation of FPGA Based Software Defined Radio Using Simulink HDL Coder," *Engineering and Technology Journal, Iraq, ISSN 1681-6900*, vol. 28, pp. 6750-6767, 2010.
- [2] M. L. Dickens, B. P. Dunn and L. J. Nicholas, "Design and Implementation of a Portable Software Radio," in *IEEE Communications Magazine*, vol. 46, no. 8, pp. 58-66, August 2008.
- [3] Kamble. Priyanka. S, "A review paper on Software Defined Radio", International Journal of Emerging Technologies and Innovative Research, vol.3, Issue 6, pp. 36-40, June-2016, Available : <http://www.jetir.org/papers/JETIR1606008.pdf> [diakses 15 September 2020].
- [4] T. Ulversoy, "Software Defined Radio: Challenges and Opportunities," *IEEE Communications Surveys & Tutorials*, vol. 12, no. 4, pp. 531-550, May, 2010.
- [5] H. Taub and D. L. Schilling, "Frequency-Modulation Systems" in *Principles of Communication System*, 2nd ed. S.W Director, New York: Mc-Graw Hill, 1996, ch. 4, pp. 142-173.
- [6] B. Setiyanto, *DASAR-DASAR TELEKOMUNIKASI*, Yogyakarta : SAKTI, 2010.
- [7] M. Sanjit K, *Digital Signal Processing A Computer-Based Approach*, 2nd ed. Mc-Graw Hill, 2002.
- [8] Tutorialspoint, *Analog Communication Tutorial*. India: Tutorialspoint, 2016. Accessed on: May. 13, 2021. [Online]. Available: https://www.tutorialspoint.com/analog_communication/index.htm
- [9] M. Shiwen, H. Yinsong, L Yihan, "On Developing a Software Defined Radio Laboratory Course for Undergraduate Wireless Engineering Curriculum," in 2014 ASEE Annual Conference & Exposition, Indianapolis, 2014.